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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/697,111 10/31/2003		Chaur-Chin Yang	BHT-3183-56 8447	
7590 08/10/2004			EXAMINER	
BRUCE H. TROXELL SUITE 1404			CLARK, JASMINE JHIHAN B	
5205 LEESBURG PIKE			ART UNIT	PAPER NUMBER
FALLS CHURCH, VA 22041			2815	

DATE MAILED: 08/10/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/697,111	YANG ET AL.				
Office Action Summary	Examiner	Art Unit				
	Jasmine J Clark	2815				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	6(a). In no event, however, may a reply be tim within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONEI	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on						
2a) This action is FINAL . 2b) This						
3) Since this application is in condition for allowan	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	33 O.G. 213.				
Disposition of Claims						
4) Claim(s) <u>1-17</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5)⊠ Claim(s) <u>1-17</u> is/are allowed.						
6) Claim(s) is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	election requirement.					
Application Papers		· (
9)⊠ The specification is objected to by the Examine	r.	1.				
10) \boxtimes The drawing(s) filed on <u>31 October 2003</u> is/are: a) \boxtimes accepted or b) \square objected to by the Examiner.						
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correcting 11) The oath or declaration is objected to by the Ex						
Priority under 35 U.S.C. § 119						
a) ☐ Acknowledgment is made of a claim for foreign a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority documents 2. ☐ Certified copies of the priority documents 3. ☐ Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No ed in this National Stage				
Attachment(s)						
Attachment(s) 1) X Notice of References Cited (PTO-892)	4) 🔲 Interview Summary	(PTO-413)				
 Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>10/31/03</u>. 	Paper No(s)/Mail Da					

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DETAILED ACTION

Specification

1. The disclosure is objected to because of the following informalities: in page 3, lines 17 and 19, for example, change "The substrate 1<u>00</u>" to --The substrate 1<u>10</u>--.. In page 5, line 22 "surface211" needs a space between "surface" and "211".

Appropriate correction is required.

References Cited

2. The references of interest are cited:

Hikita et al. (US 6,462,420 B2) and Morifuji (US 6,175,157 B1) teach a semiconductor device having dummy bumps. Pai et al. (US 6,503,776 B2), Lo et al. (US 6,414,384 B1), and Nishiuma et al. (US 5,616,520) teach a semiconductor device including dummy chip, but fail to teach that the dummy chip has a distribution layer (RDL) which attached to a first flip chip; at least a second flip chip mounted on the dummy chip and electrically connected to the RDL; and a plurality of electrically connecting devices connecting the dummy chip with a substrate where the first flip chip disposed thereon.

Conclusion

3. This application is in condition for allowance except for the following formal matters:

Please see the above discussion.

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Prosecution on the merits is closed in accordance with the practice under Exparte Quayle, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO**MONTHS from the mailing date of this letter.

Telephone Inquiry Contacts

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jasmine J Clark whose telephone number is (571) 272-1726. The examiner can normally be reached on Flex.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jjbc/08/05/04

JASMINE CLARK
PRIMARY EXAMINER

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